

Claim Rejections - 35 USC §112

In the Office Action, the Examiner rejects Claims 10-14, 16-23, 25-27, 68-71 and 76-83 under 35 USC §112, first paragraph, as failing to comply with the enablement requirement. This rejection is respectfully traversed.

In particular, the Examiner alleges that “Claims 10-14, 16-23, 25-27, 68-71 and 76-83, disclosed that a first conductive layer comprising a gate electrode and a separate second conductive layer comprising gate wiring and they are in contact with each other. However, neither the specification nor the drawings disclose how they are in contact with each other.” Applicants respectfully disagree.

For example, the present application discloses a first conductive layer comprising a gate electrode at, at least, reference numeral 128 (e.g. p. 12, ln. 25; p. 14, lns. 3-4; p. 16, lns. 3-5), and a second conductive layer comprising a gate wiring at, at least, reference numeral 147 (e.g. p. 17, lns. 7-8, 16-18). Figs. 6B and 6C show that at least gate electrode 128 is in electrical contact with gate wiring 147 (see also p. 18, lns. 1-7). Hence, these features are clearly described in the specification and drawings in such a way as to enable one skilled in the art to make and/or use the invention, and the enablement requirement has clearly been satisfied.

Accordingly, it is respectfully requested that this rejection be withdrawn.

Conclusion

As Applicants have previously overcome all the prior art and double patenting rejections and have now shown that the present application and claimed invention are enabling under §112,

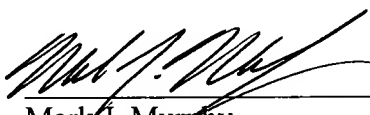
it is respectfully submitted that the present application is in a condition for allowance and should be allowed.

If any fee is due for this response, please charge our deposit account 50/1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

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